

Title: A FEED FORWARD EQUALIZER  
AND A METHOD FOR ANALOG  
EQUALIZATION OF A DATA SIGNAL  
Applicant(s): Peter Gregorius  
Atty. Docket No.: 1406/143/2

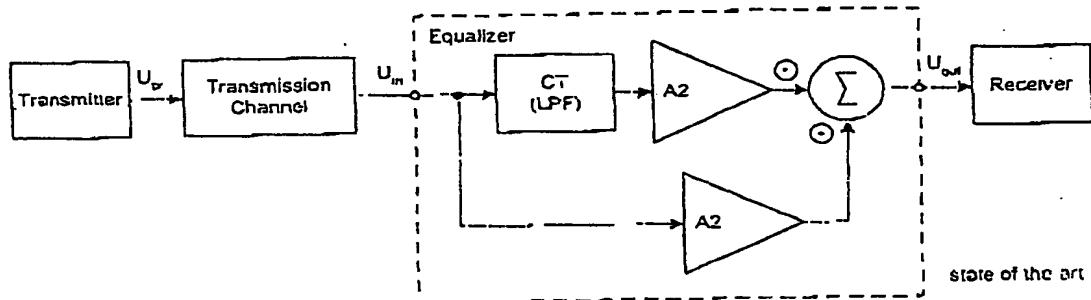


Fig. 1

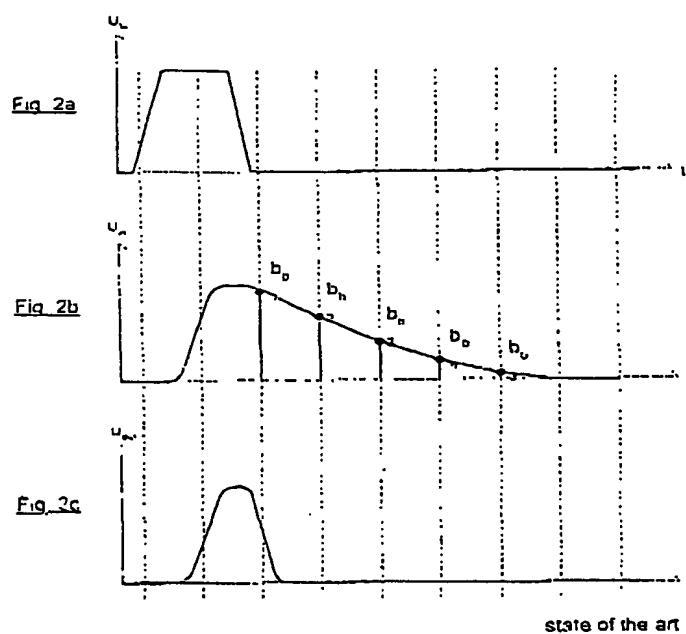


Fig. 2

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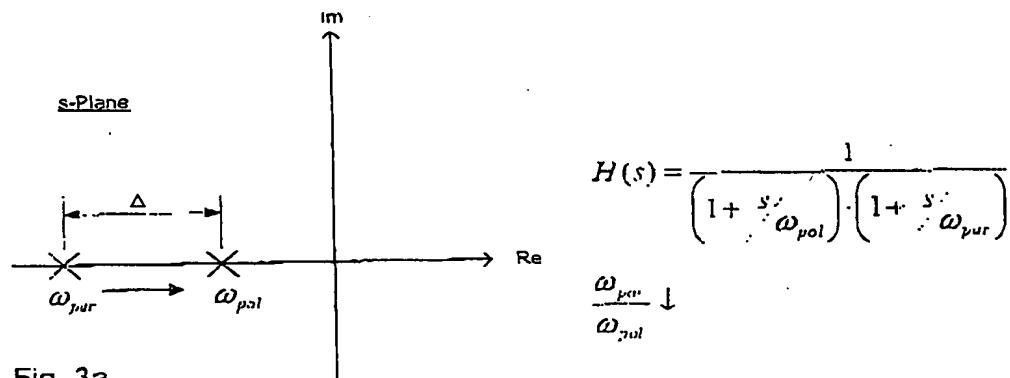


Fig. 3a

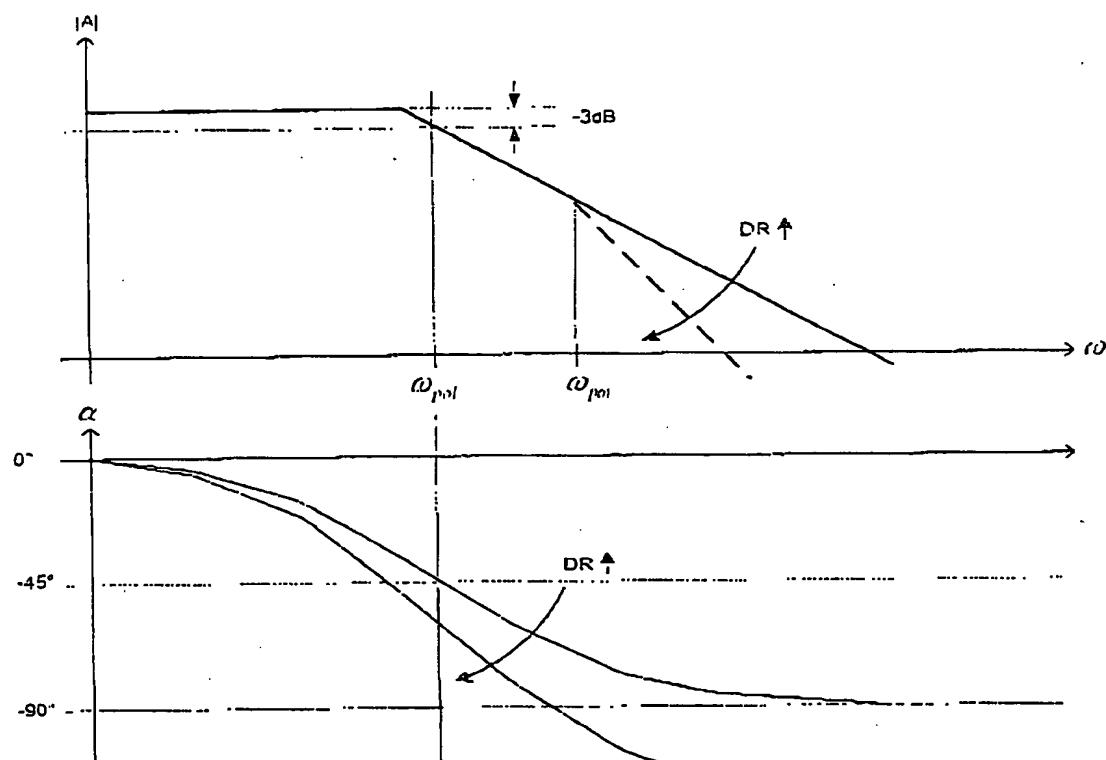


Fig. 3b

Fig. 3

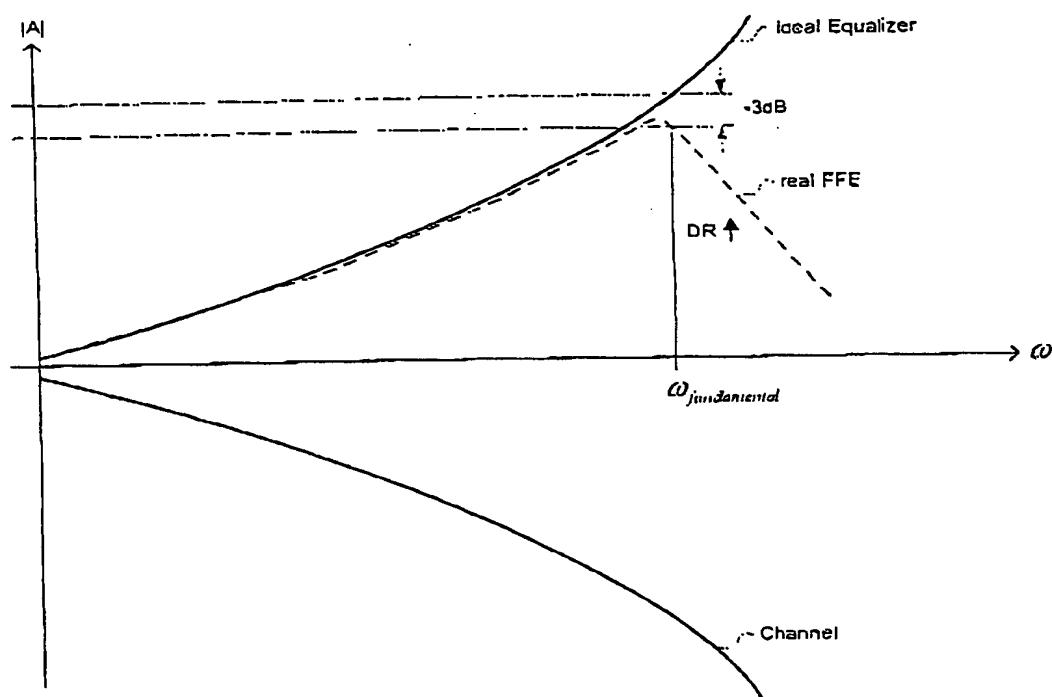


Fig. 4

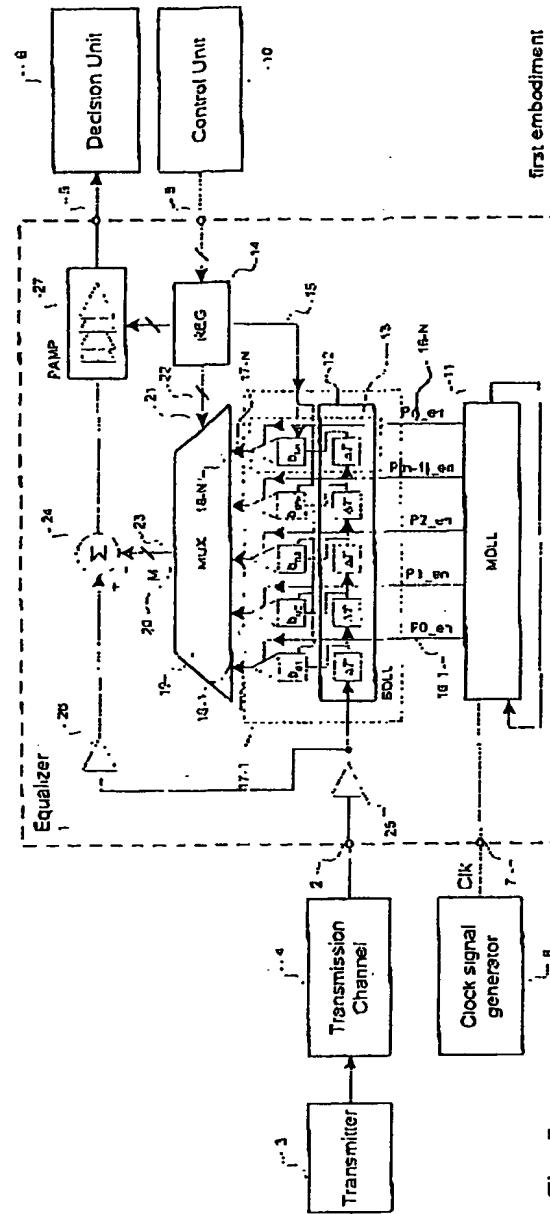


Fig. 5

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Fig. 6a



Fig. 6b

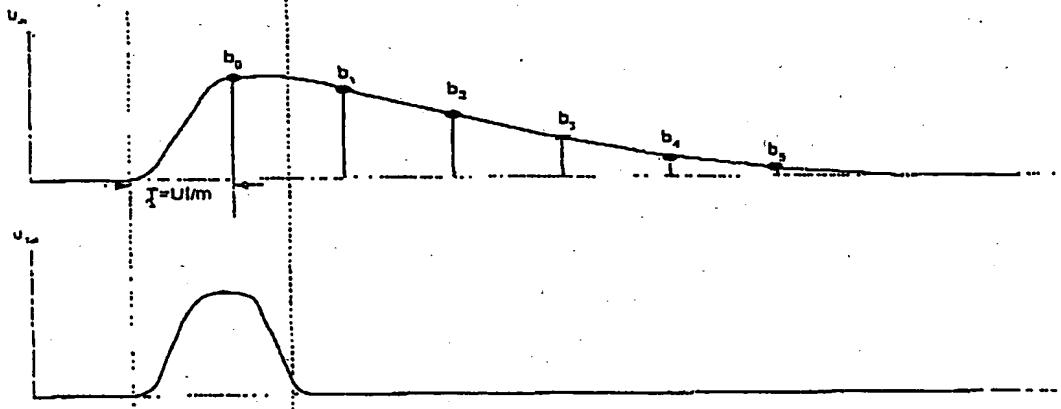


Fig. 6c

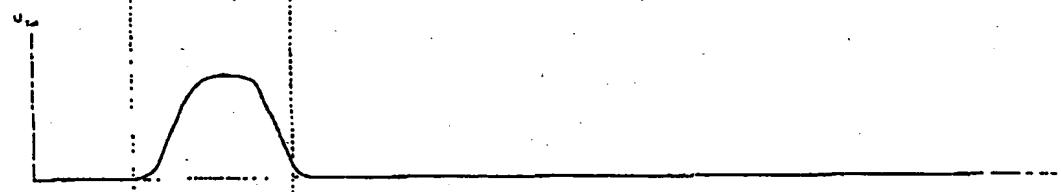
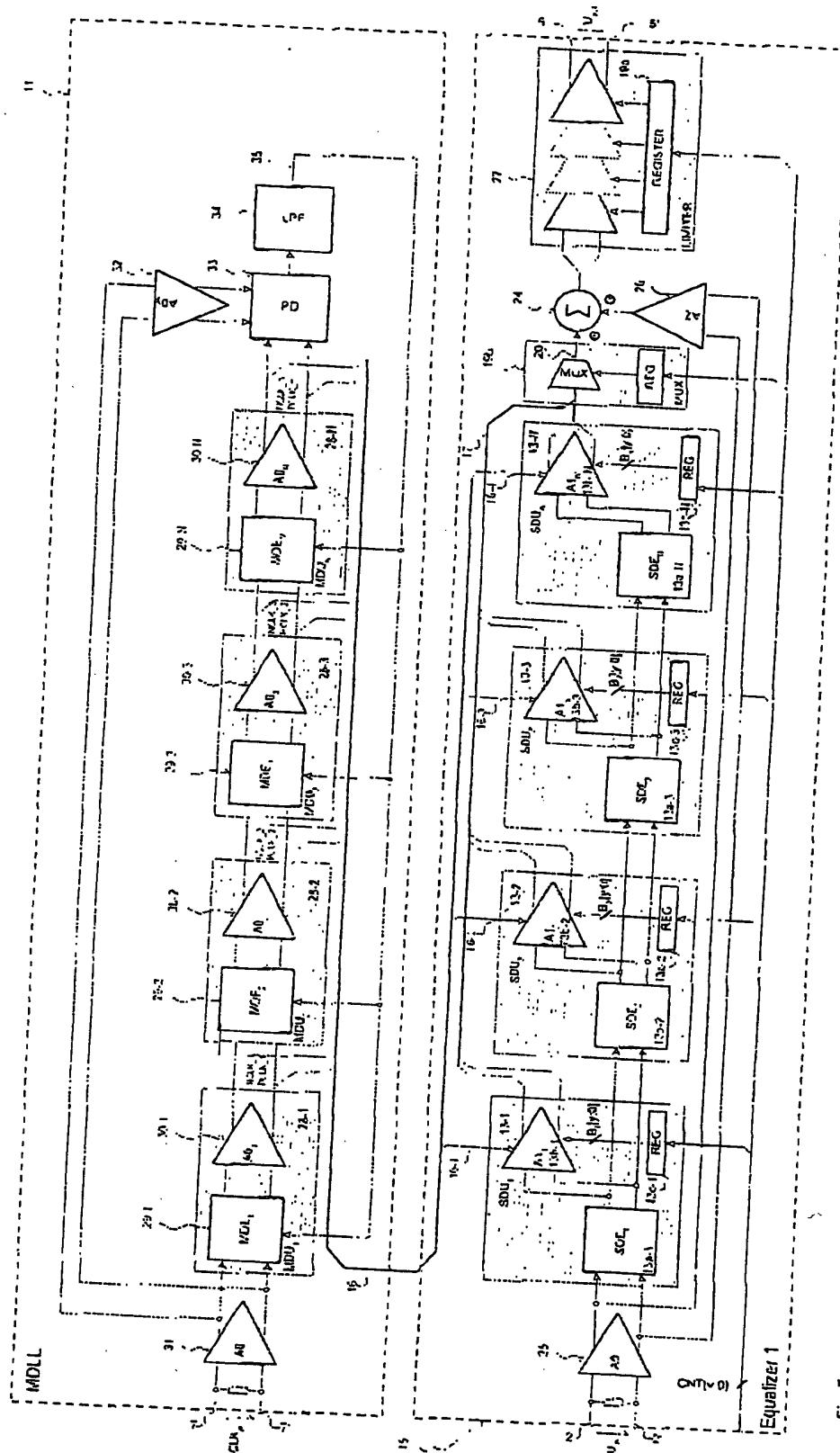
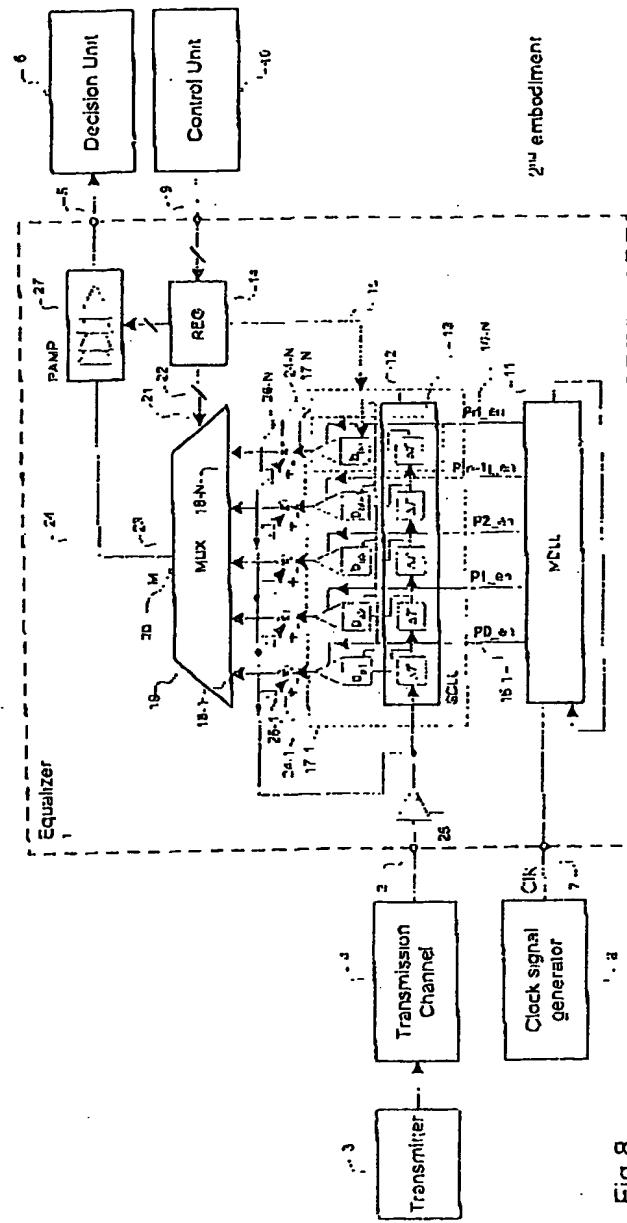


Fig. 6

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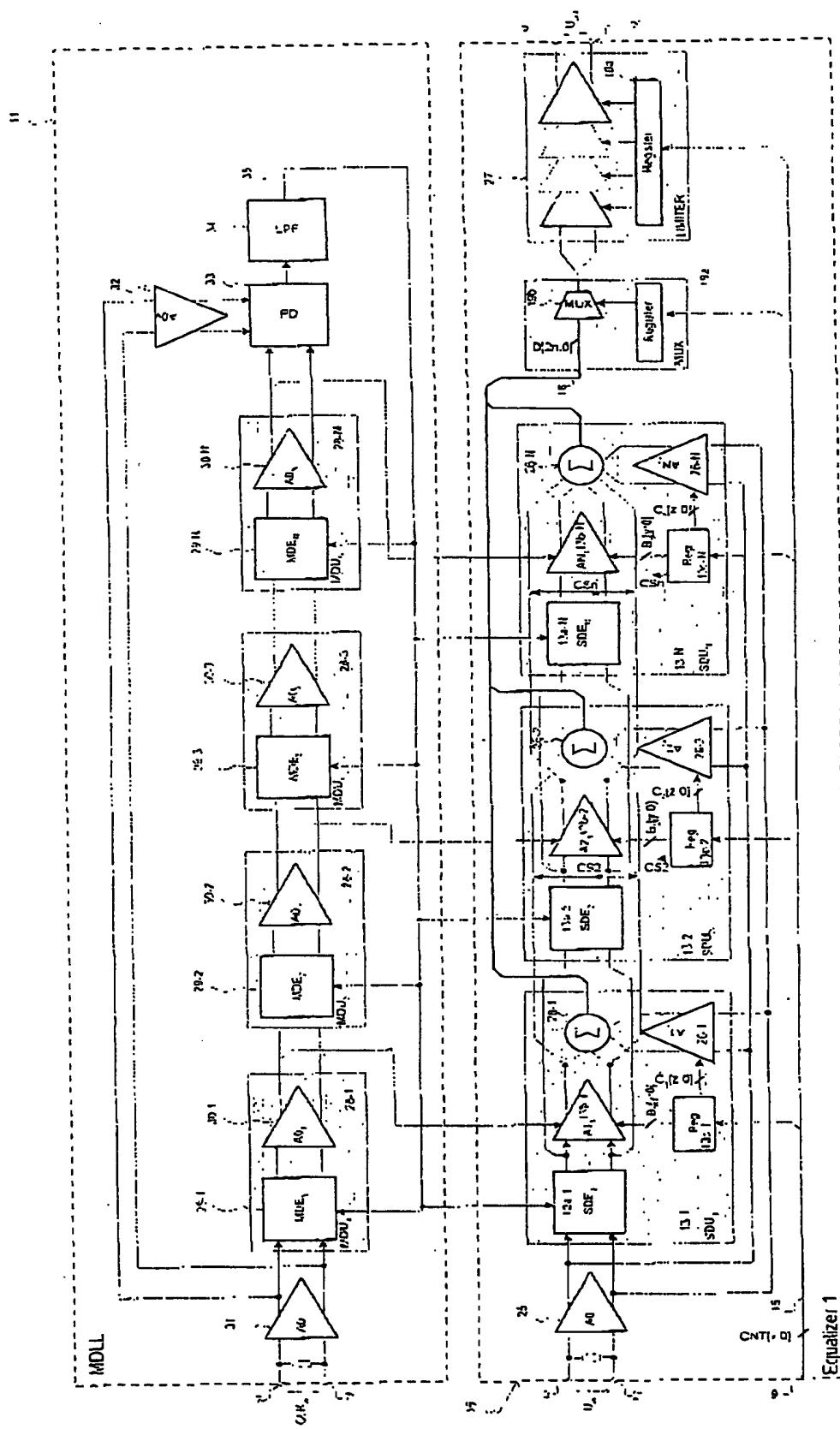


Fig. 9

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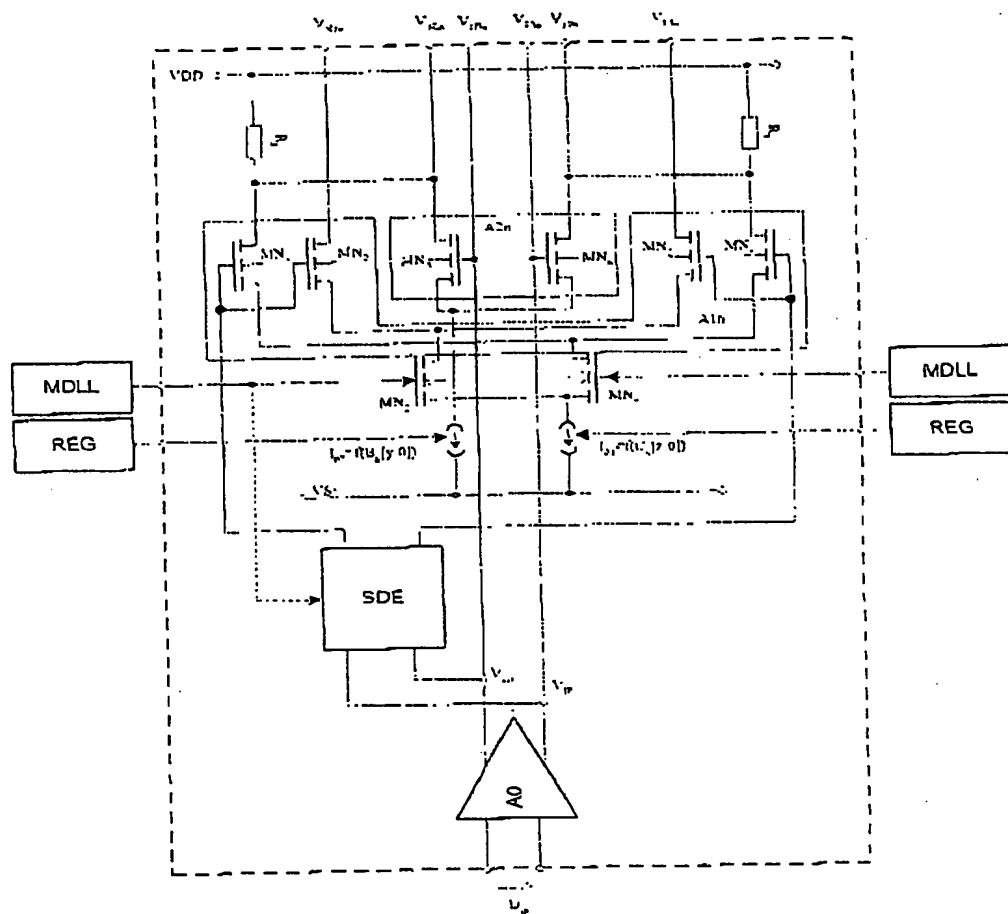


Fig. 10

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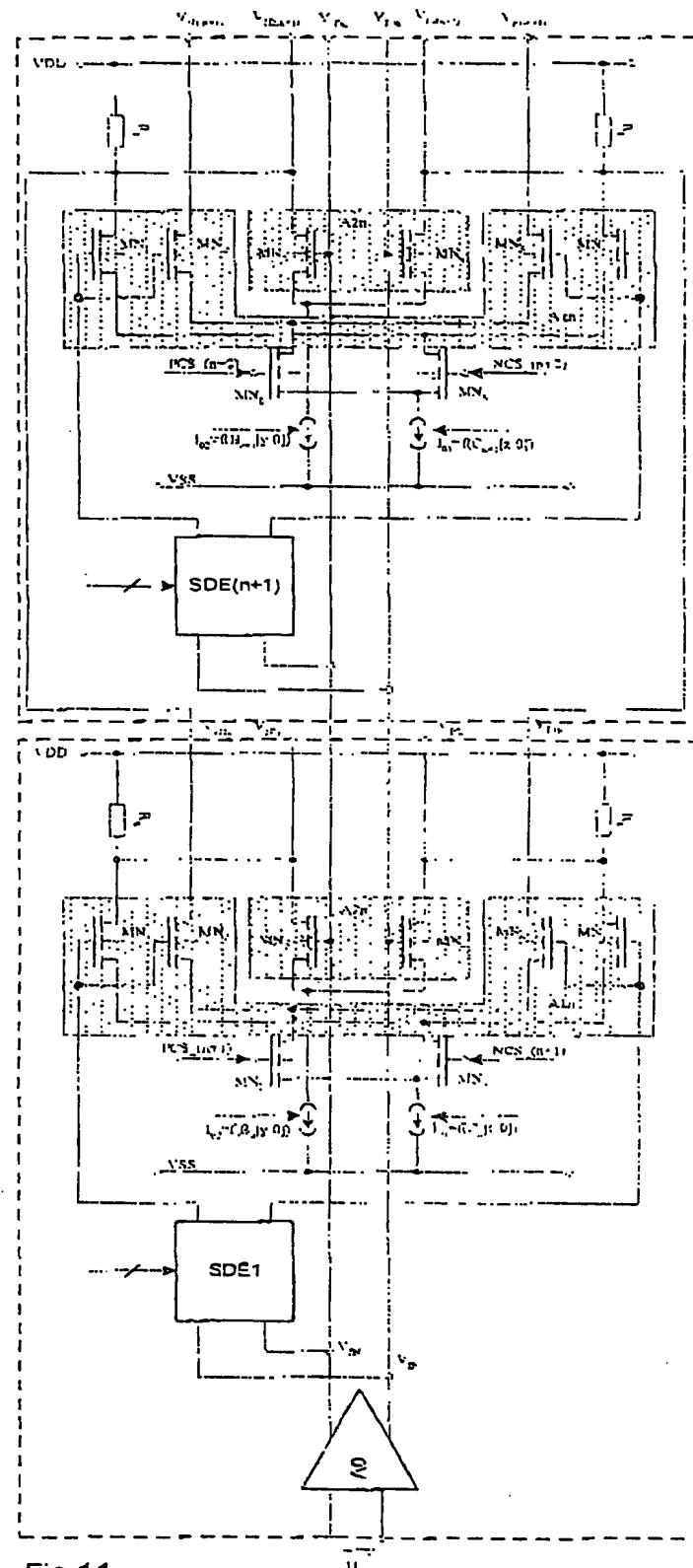


Fig.11